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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10080568	FILING DATE 02/25/2002	CLASS 428 25	SUBCLASS 7 20	GAU 2812	EXAMINER P. CAO
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\*\*APPLICANTS: Gates Stephen; Scheuerlein Roy;

\*\*CONTINUING DATA VERIFIED:

THIS APPLICATION IS A DIV OF 09/427,251 10/26/1999

\*\* FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO YOR919980324 US2
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		
Verified and Acknowledged Examiners's initials		
TITLE : Formation of arrays of microelectronic elements		

U.S. DEPT. OF COMM. / PAT. & TM. PTO-436L (Rev. 12-94)

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Assistant Examiner	Total Claims 17
			Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Primary Examiner	Sheets Drwg. Figs. Drwg. Print Fig.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	Application Examiner
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